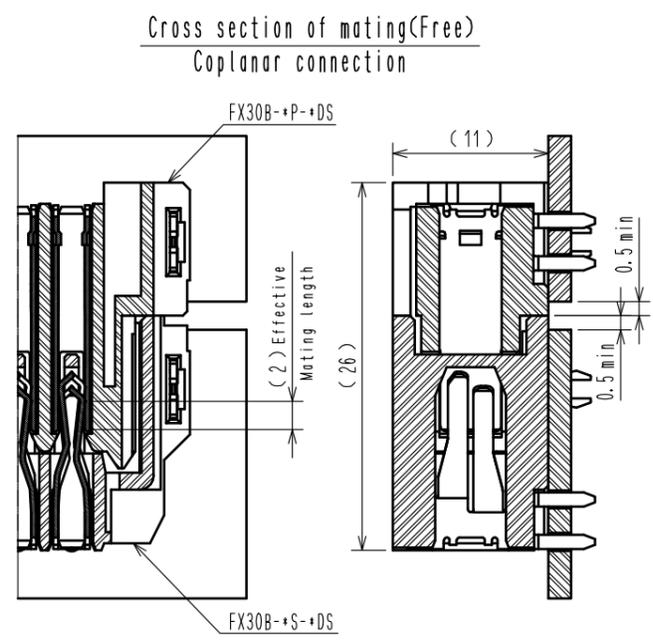
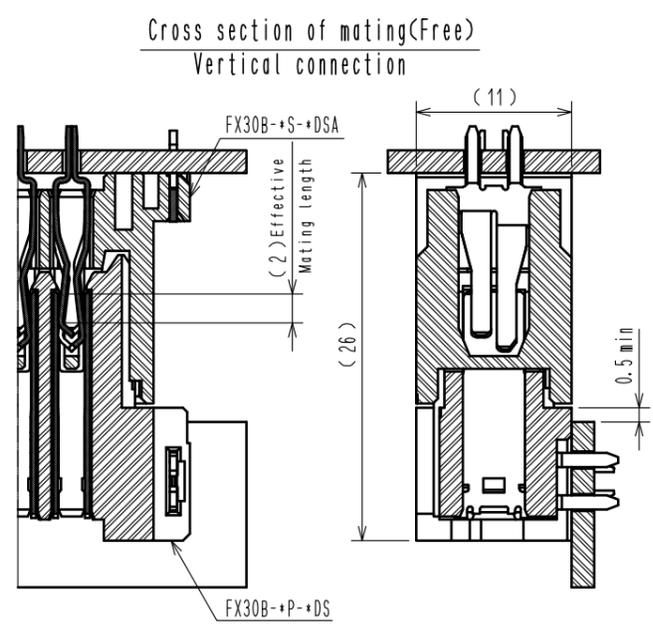
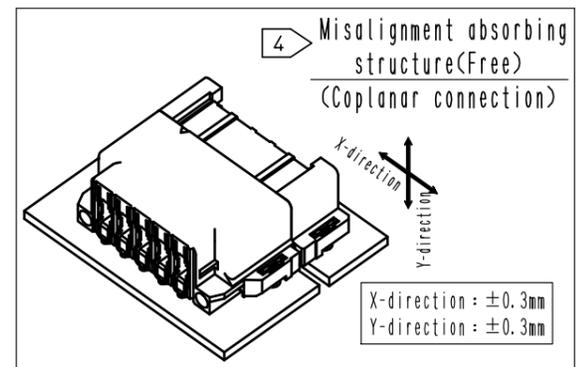
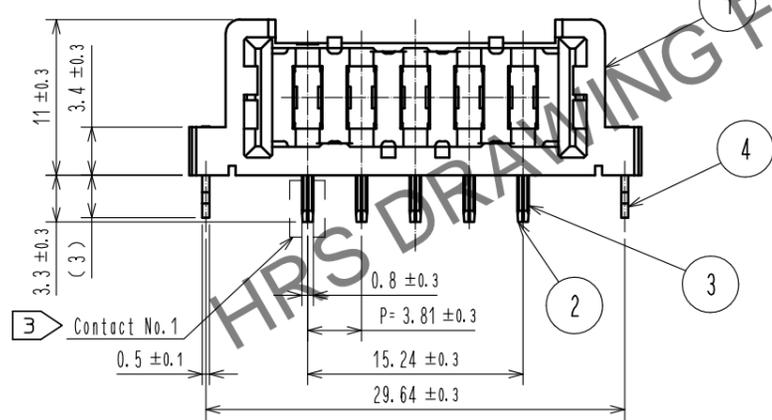
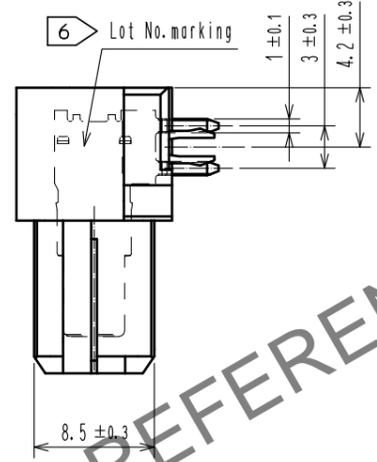
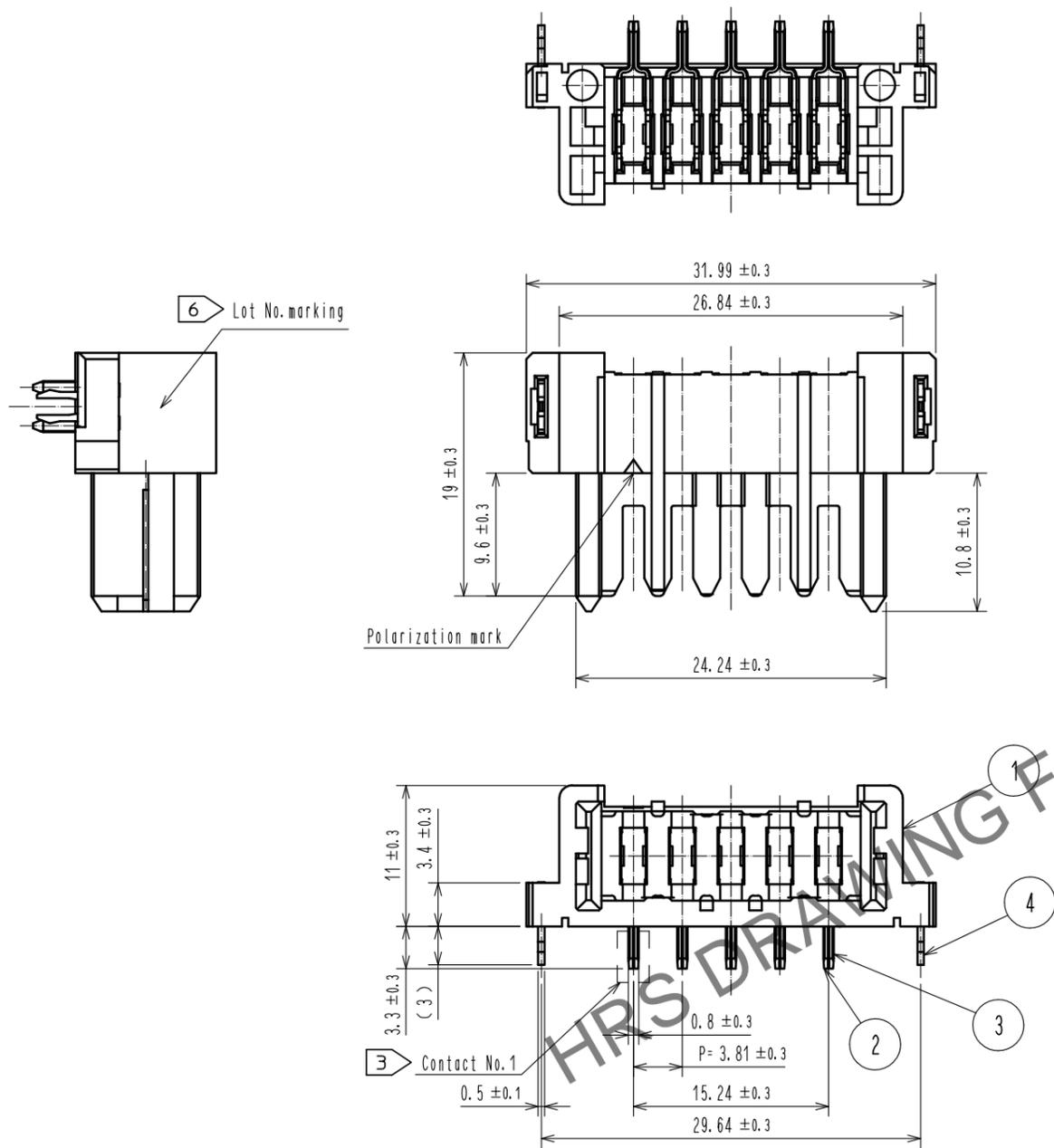


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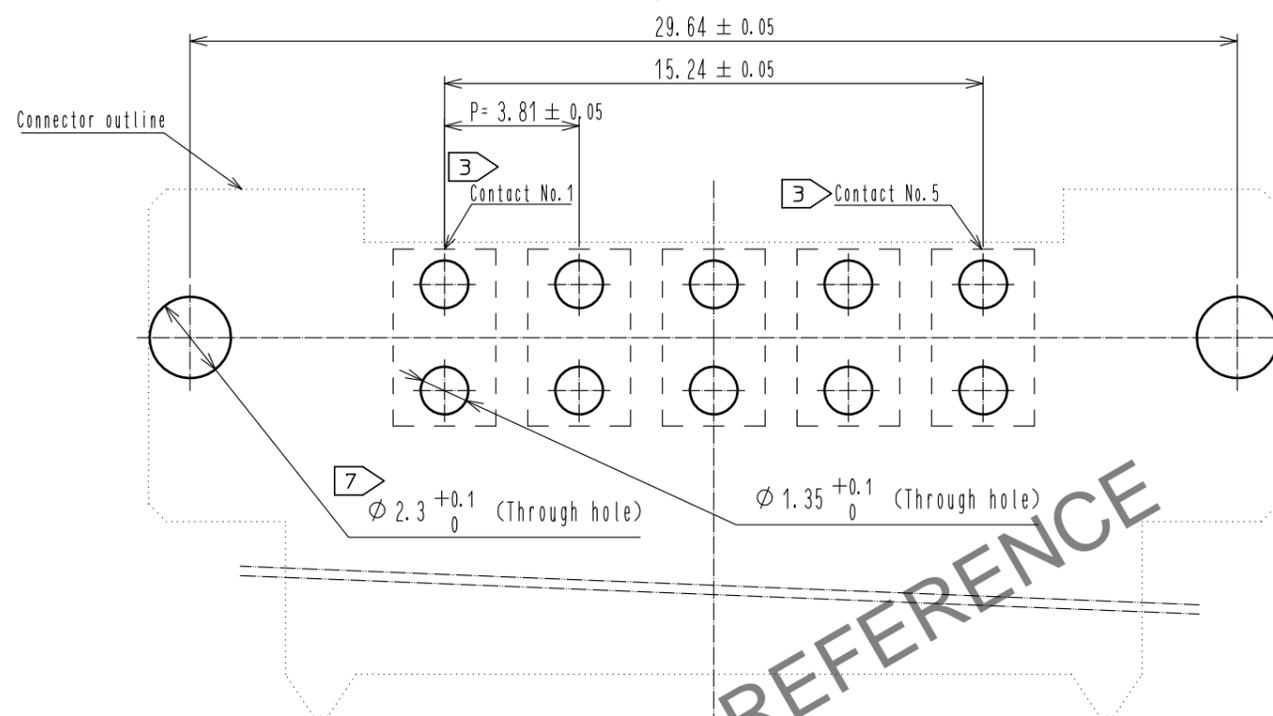
- Note
- ① This product is packaged in tray. (20pcs/tray)
 - ② Blemish and hit mark can be occurred through out the manufacturing process which dosen't affect quality level. Also, black spot could be observed on the surface of housing.
 - ③ For the contact No., adjacent two pairs of leads placed back-to-back (4 leads in total) compose one line.
 - ④ Misalignment absorbing range: ±0.3mm max.
 - ⑤ The dimension in parentheses are for references.
 - ⑥ Lot No. is indicated at either one of the position as shown.
 - ⑦ Pay attention to the recommended PCB thickness and finished through-hole dimensions. Low retention force to PCB could induce angled mounting due to the unbalanced center of the connector.
 - ⚠ ⑧ UL(File No. E52653)
C-UL(File No. E52653)
TUV(Certificate No. R50275872) authorization article.



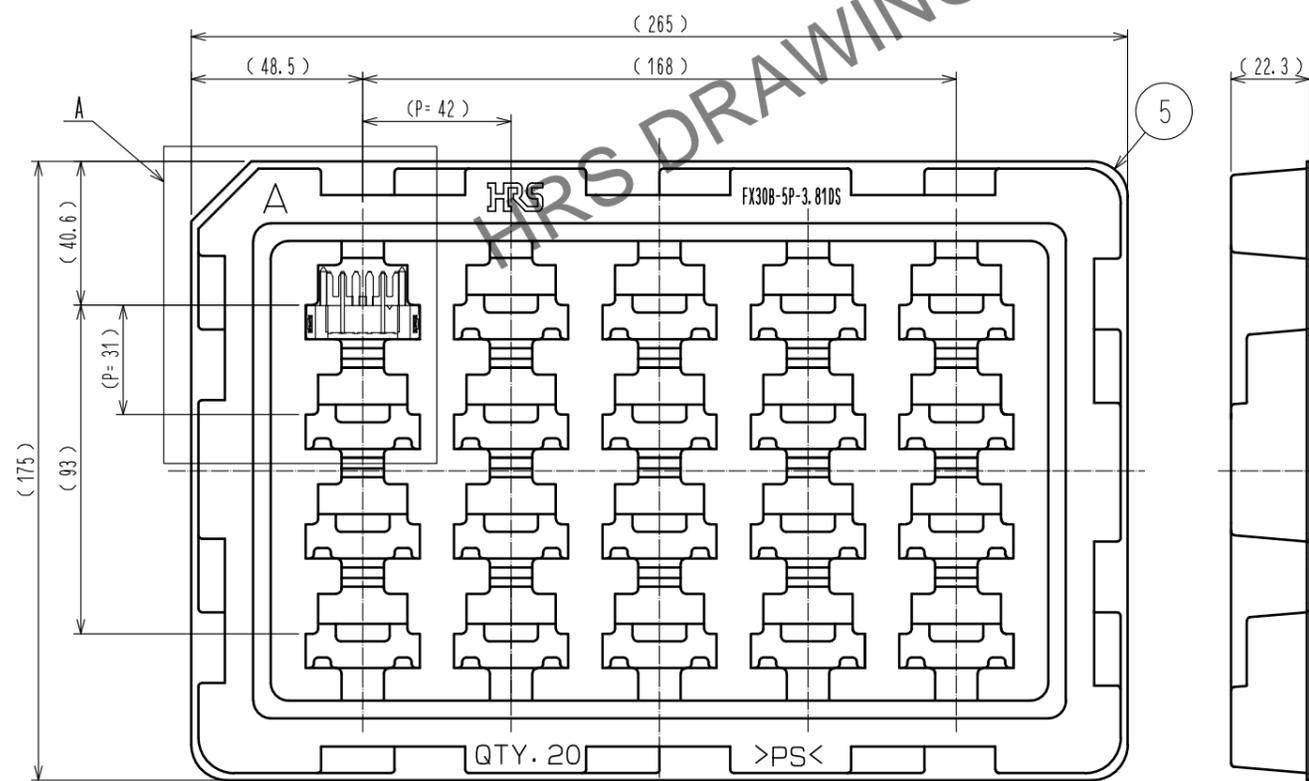
2	COPPER ALLOY	CONTACT AREA:GOLD-PLATING 0.1 μm LEAD AREA:TIN-PLATING 1.0 μm UNDER PLATING:NICKEL 1.0 μm	5	POLYSTYRENE	
1	POLYAMIDE	BLACK UL94V-0	4	PHOSPHOR BRONZE	TIN-PLATING 3.0 μm UNDER PLATING:NICKEL 1.0 μm
			3	COPPER ALLOY	CONTACT AREA:GOLD-PLATING 0.1 μm LEAD AREA:TIN-PLATING 1.0 μm UNDER PLATING:NICKEL 1.0 μm
NO.	MATERIAL	FINISH . REMARKS	NO.	MATERIAL	FINISH . REMARKS
UNITS mm		SCALE 2 : 1	COUNT 1	DESCRIPTION OF REVISIONS DIS-F-00006019	DESIGNED AK. IWAHORI
		APPROVED : HS. OKAWA 20130313	DRAWING NO. EDC3-347271-00		DATE 20200406
HIROSE ELECTRIC CO., LTD.		CHECED : KI. HIROKAWA 20130312	PART NO. FX30B-5P-3.81DS		
		DESIGNED : DK. AIMOTO 20130311	CODE NO. CL570-3403-5-00		
		DRAWN : DK. AIMOTO 20130311			

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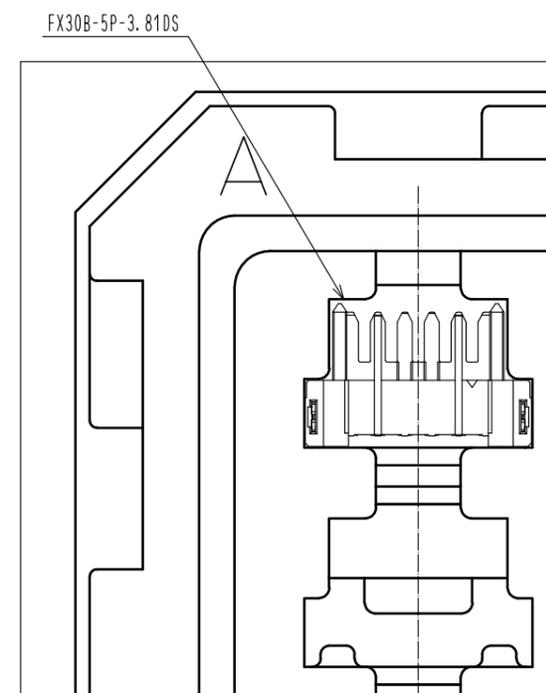
Recommended hole pattern dimension of PCB(5:1)
 (Connector mounting side, PCB thickness:t=1.6mm) \triangleleft 7



1 Drawing for packaging(1:2)



A(1:1)



HRS	DRAWING NO.	EDC3-347271-00	$\frac{2}{2}$
	PART NO.	FX30B-5P-3.81DS	
	CODE NO.	CL570-3403-5-00	