






Open Mode Chip Varistors

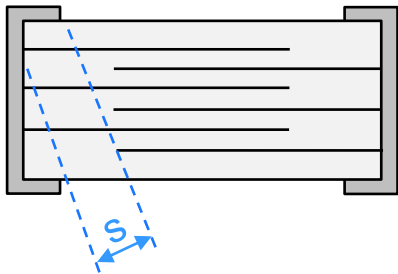
Failsafe protection for automotive battery lines

Features



-  **Open Mode design**
-  **Failsafe acc. VW 80808-1, issue 2020-03**
-  **Qualified based on AEC-Q200 and VW 80808-2**
-  **Protection against pulses of ISO 7637-2**
-  **Available in EIA 0805, 1206, 1210**






Design



safety margin

Multilayer Open Mode design with defined safety margin avoid shorts even after excessive bending in accordance to new VW standard 80808-1.

Reliability & pulse robustness

-  Robustness acc. to VW 80808-2, issue 2020-03
-  60.000 pulses acc. VW TL 81.000 pulse 1
-  ≥ 5.000 pulses acc. ISO 7637-2
-  Pulse robustness from -55°C to $+150^{\circ}\text{C}$
-  Failsafe up to 5 mm bending



Types & ordering codes

EIA 0805:	B72510G1140S862	CT0805S14BAUTOGOM_G
EIA 1206:	B72520G1140S862	CT1206S14BAUTOGOM_G
EIA 1210:	B72530G1140S862	CT1210S14BAUTOGOM_G